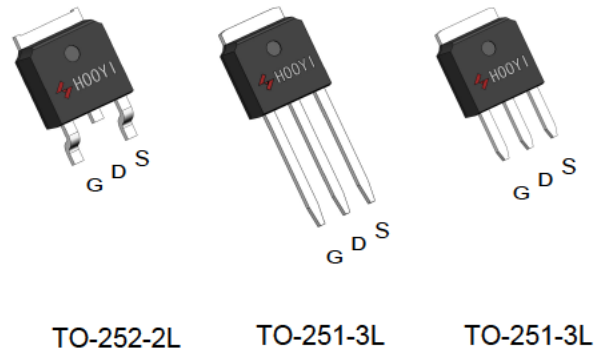


N-Channel Enhancement Mode MOSFET

Features

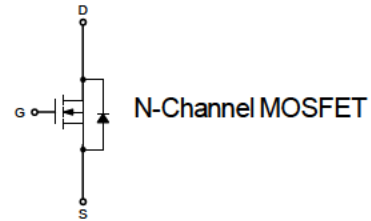
- 40V/80A,
 $R_{DS(ON)} = 4.0 \text{ m}\Omega$ (typ.) @ $V_{GS} = 10\text{V}$
- Avalanche Rated
- Reliable and Rugged
- Lead Free and Green Devices Available
 (RoHS Compliant)

Pin Description






Applications

- Power Management for Inverter Systems.



Ordering and Marking Information

 D HY1804 YYXXJWW G	 U HY1804 YYXXJWW G	 S HY1804 YYXXJWW G	Package Code D : TO-252-2L U : TO-251-3L S : TO-251-3L
Date Code YYXX WW			Assembly Material G : Lead Free Device

Note: HOOYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. HOOYI lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. HOOYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HOOYI reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_C=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage	40	V	
V_{GSS}	Gate-Source Voltage	± 20		
T_J	Maximum Junction Temperature	175	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ\text{C}$	
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	80	A
Mounted on Large Heat Sink				
I_{DM}	Pulsed Drain Current *	$T_C=25^\circ\text{C}$	320**	A
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	80	A
		$T_C=100^\circ\text{C}$	54	
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	62.5	W
		$T_C=100^\circ\text{C}$	31.2	
$R_{\theta JC}$	Thermal Resistance-Junction to Case		2.4	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient		110	$^\circ\text{C}/\text{W}$
E_{AS}	Drain-Source Avalanche Energy	$L=0.3\text{mH}$	340***	mJ

Note : * Repetitive rating ; pulse width limited by junction temperature

** Drain current is limited by junction temperature

*** $V_D=32\text{V}$

Electrical Characteristics ($T_C = 25^\circ\text{C}$ Unless Otherwise Noted)

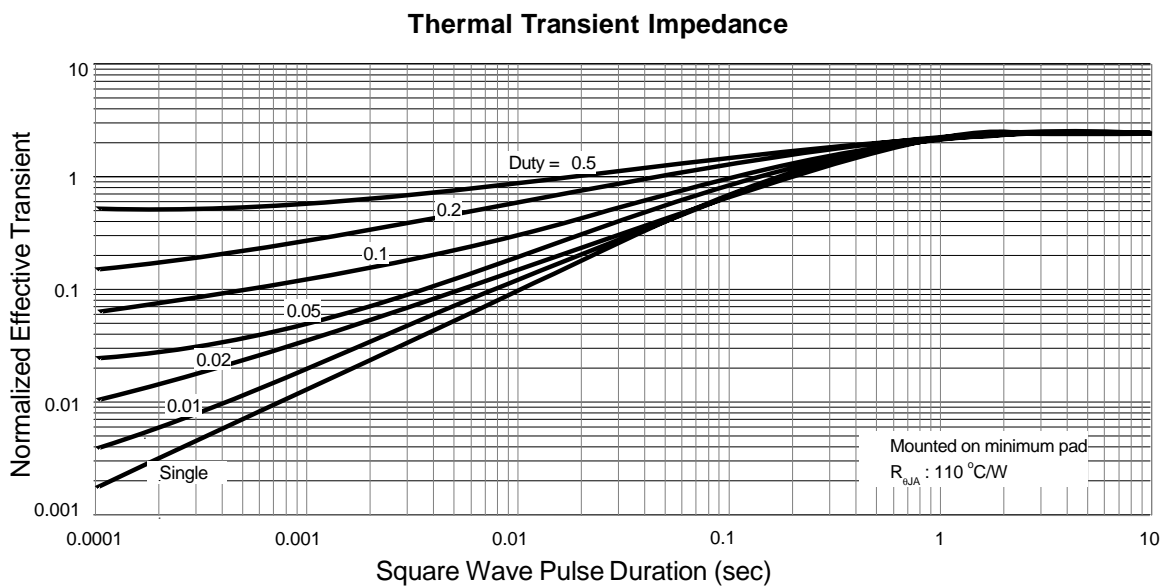
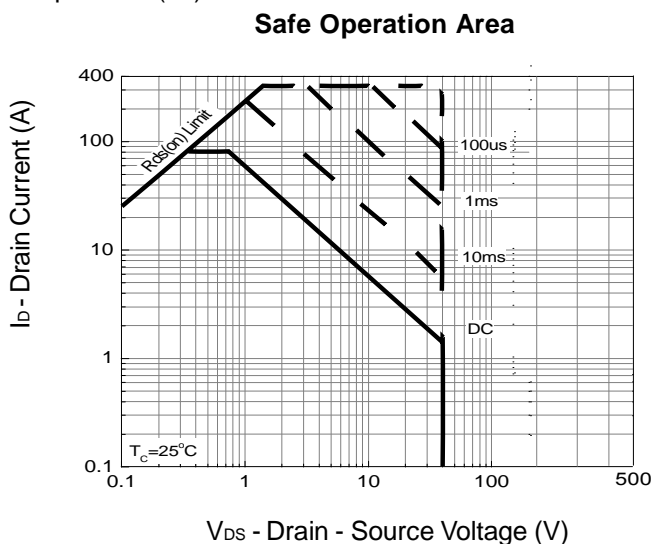
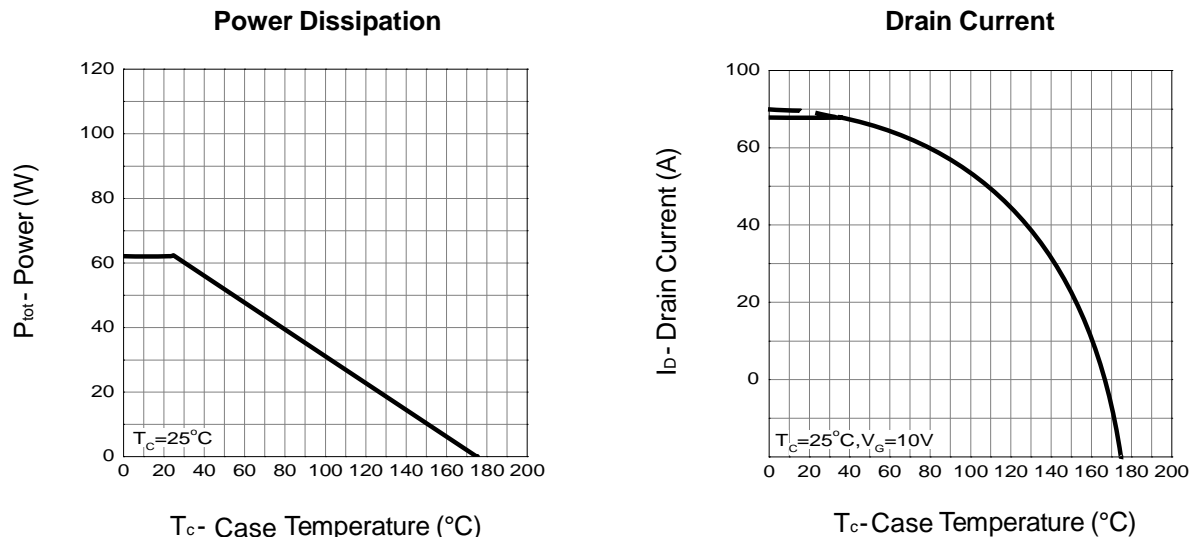
Symbol	Parameter	Test Conditions	HY1804			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_{DS}=250\mu\text{A}$	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$	-	-	1	μA
			-	-	30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu\text{A}$	1.0	2.0	3.0	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	-	-	± 100	nA
$R_{DS(ON)}^*$	Drain-Source On-state Resistance	$V_{GS}=10\text{V}, I_{DS}=40\text{A}$	-	4.0	5.0	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_{DS}=40\text{A}$		4.6	6.0	$\text{m}\Omega$
Diode Characteristics						
V_{SD}^*	Diode Forward Voltage	$I_{SD}=40\text{A}, V_{GS}=0\text{V}$	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	$I_{DS}=40\text{A}, dI_{SD}/dt=100\text{A}/\mu\text{s}$	-	50	-	ns
Q_{rr}	Reverse Recovery Charge		-	74	-	nC

Electrical Characteristics (Cont.) ($T_c = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HY1804			Unit
			Min.	Typ.	Max.	
Dynamic Characteristics						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	-	2.8	-	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=25V,$ Frequency=1.0MHz	-	4280	-	pF
C_{oss}	Output Capacitance		-	461	-	
C_{rss}	Reverse Transfer Capacitance		-	324	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=20V, R_G=4\Omega,$ $I_{DS}=40A, V_{GS}=10V,$	-	21	-	ns
T_r	Turn-on Rise Time		-	25	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	27	-	
T_f	Turn-off Fall Time		-	31	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge	$V_{DS}=32V, V_{GS}=10V,$ $I_{DS}=40A$	-	90	-	nC
Q_{gs}	Gate-Source Charge		-	7.8	-	
Q_{gd}	Gate-Drain Charge		-	19	-	

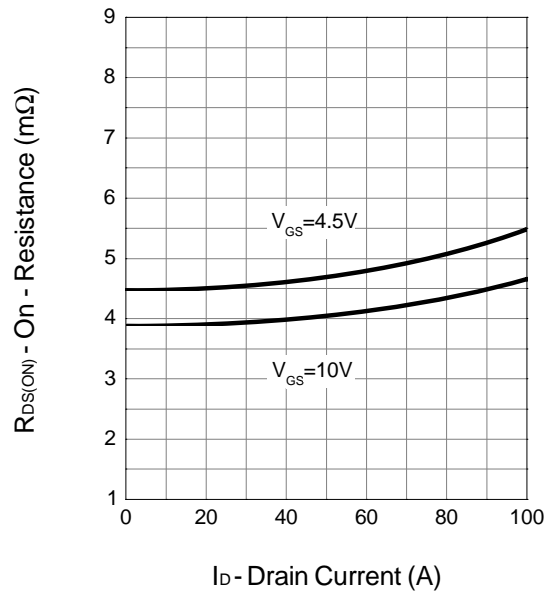
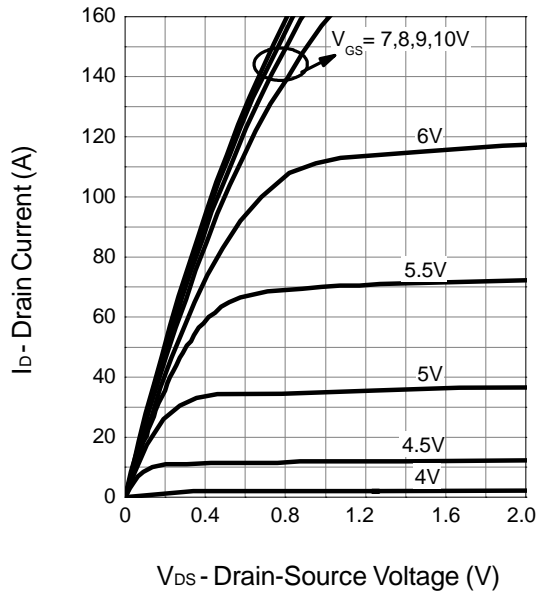
 Note * : Pulse test ; pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

Typical Operating Characteristics

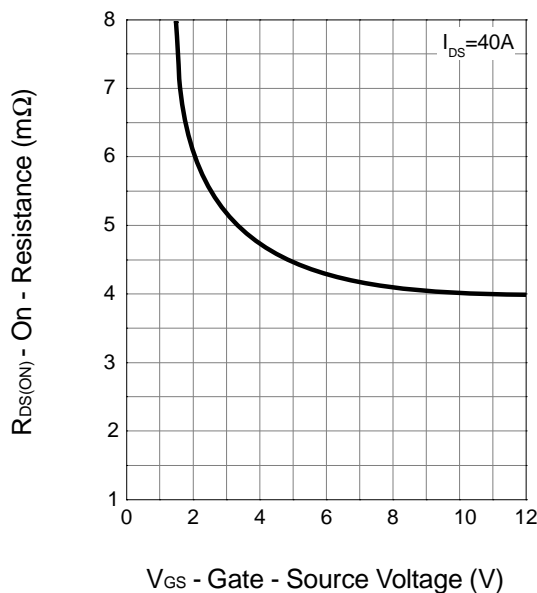


Typical Operating Characteristics (Cont.)

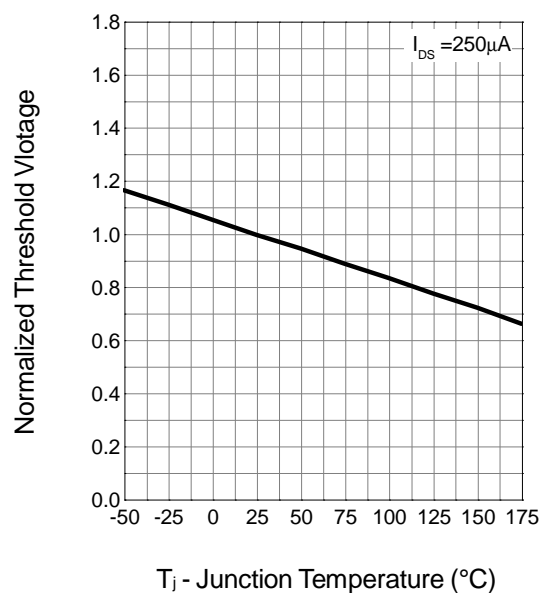
Output Characteristics



Drain-Source On Resistance

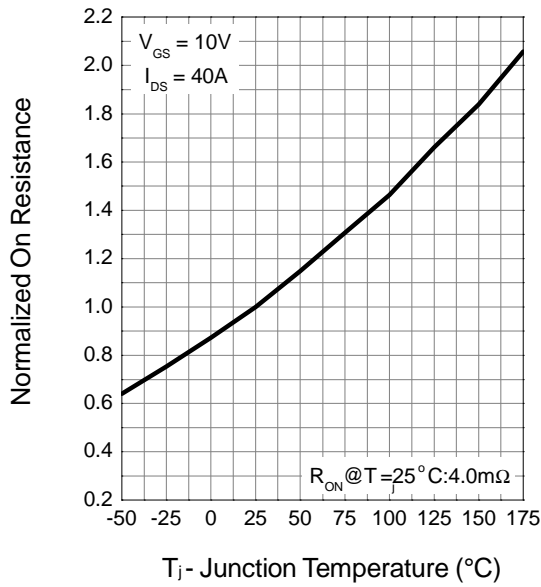


Gate Threshold Voltage

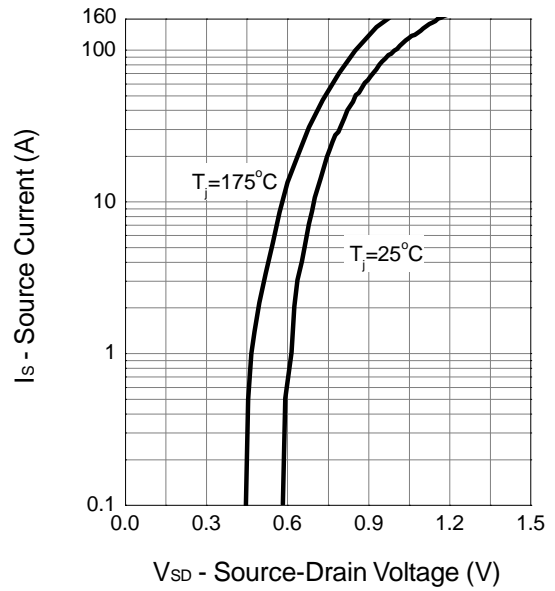


Typical Operating Characteristics (Cont.)

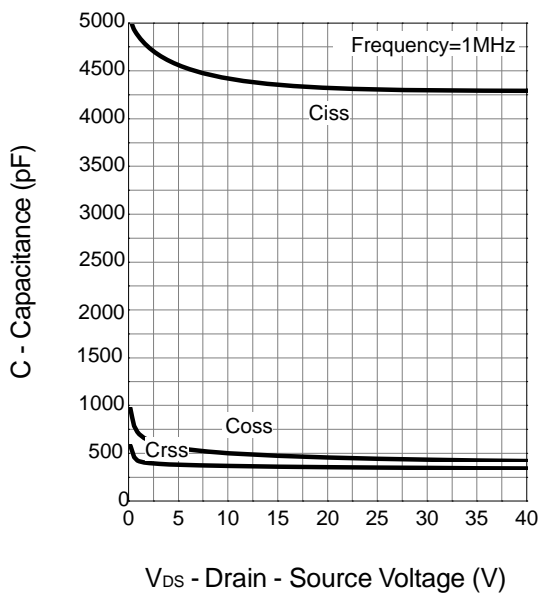
Drain-Source On Resistance



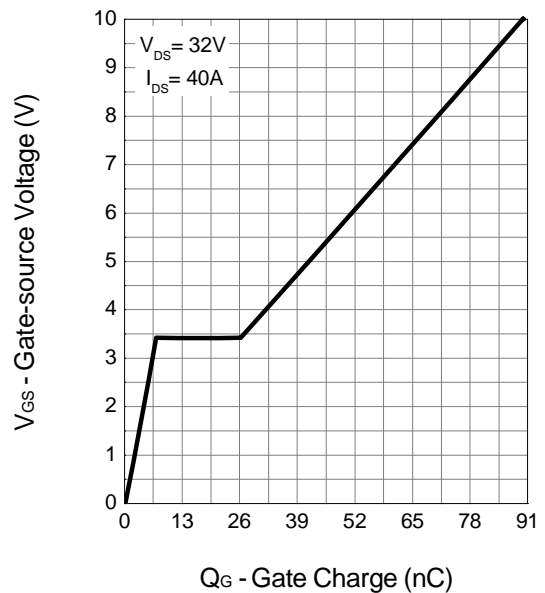
Source-Drain Diode Forward



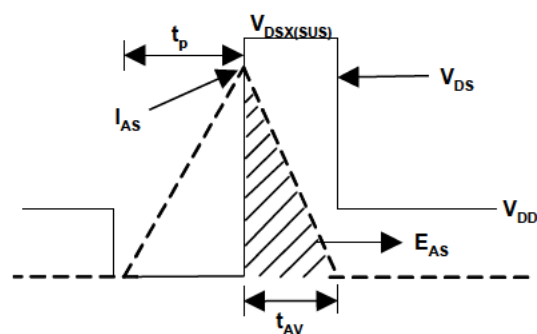
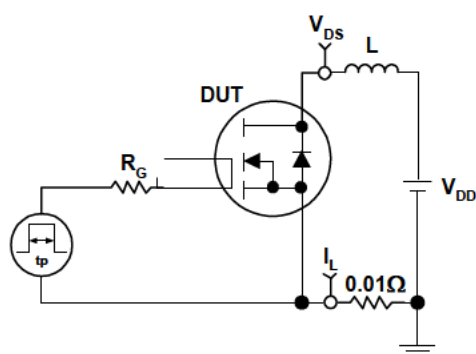
Capacitance



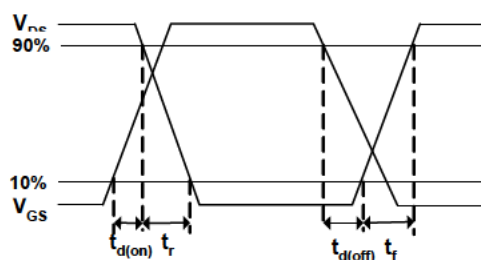
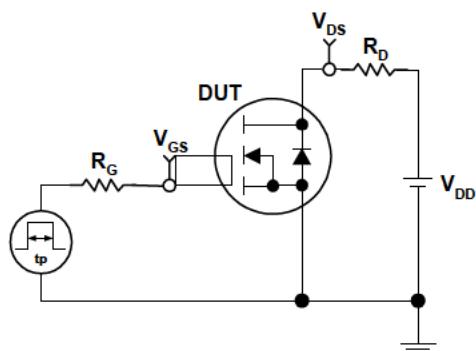
Gate Charge



Avalanche Test Circuit and Waveforms

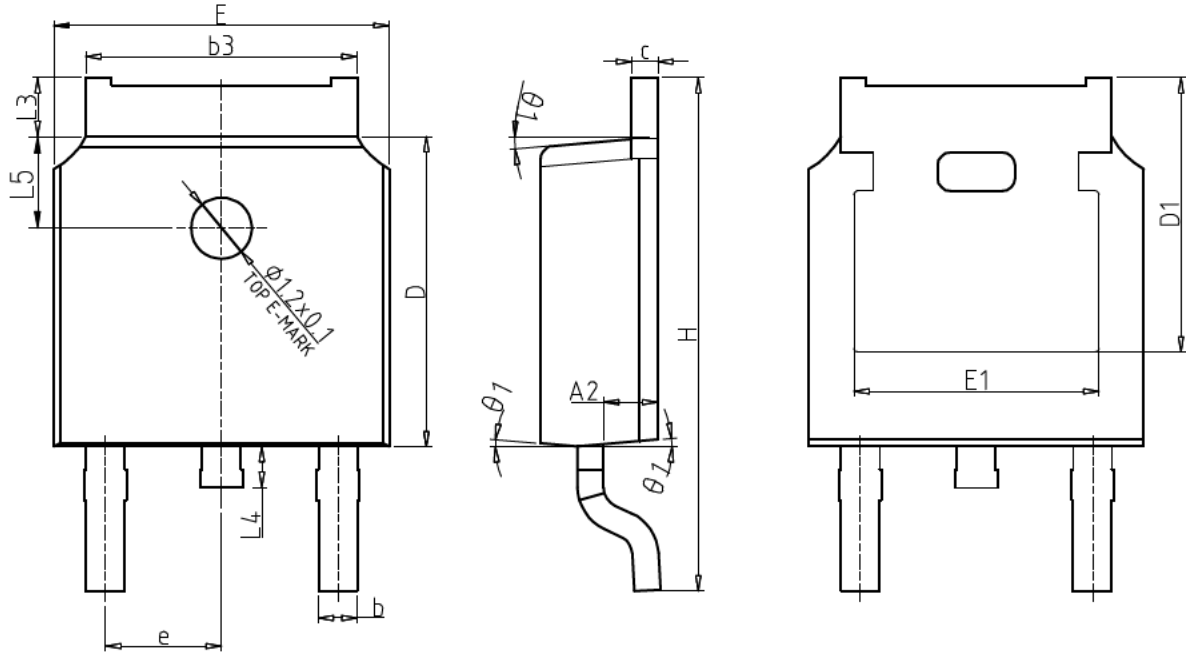


Switching Time Test Circuit and Waveforms

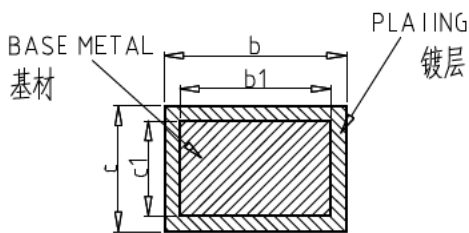
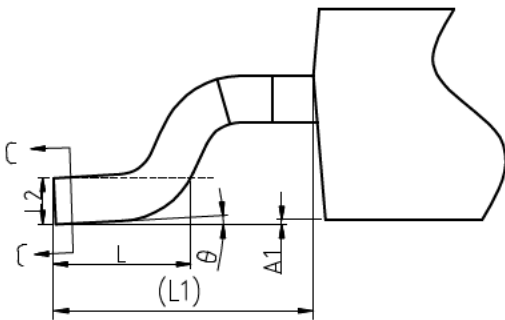
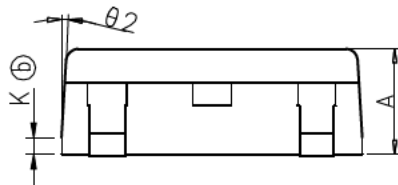


Package Information

TO-252-2L



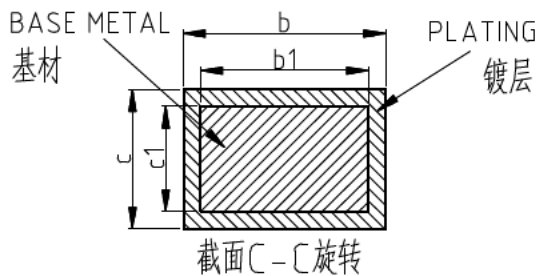
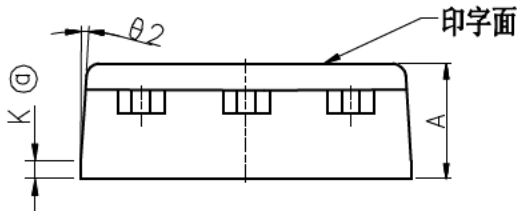
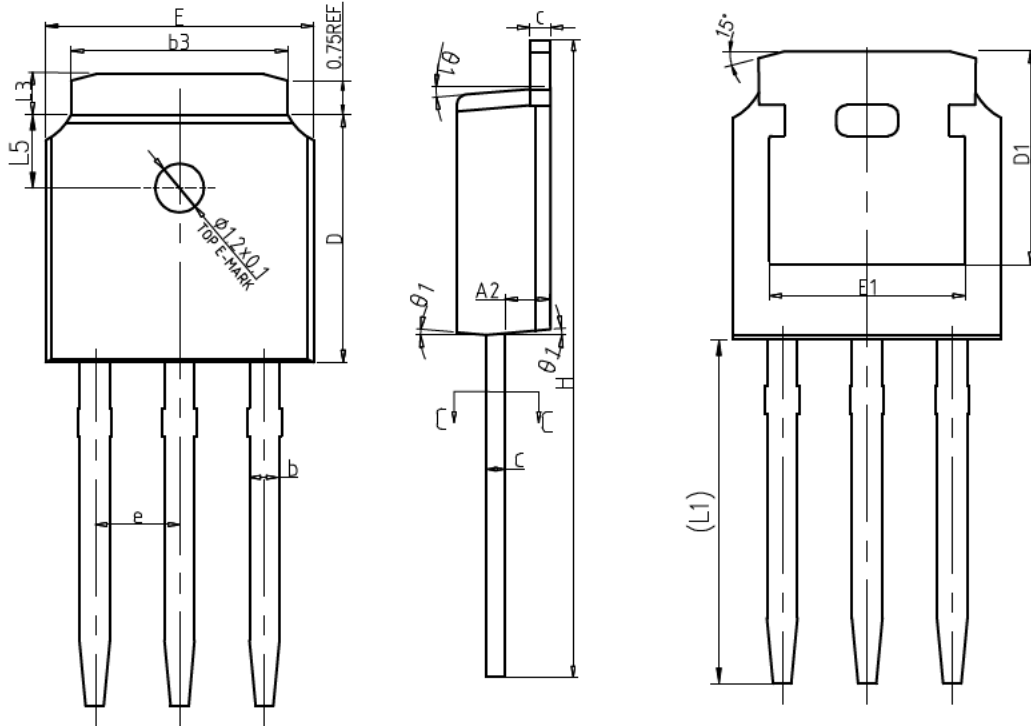
COMMON DIMENSIONS



SECTION C-C

SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	-	0.10
A2	0.97	1.07	1.17
b	0.72	0.78	0.85
b1	0.71	0.76	0.81
b3	5.23	5.33	5.46
c	0.47	0.53	0.58
c1	0.46	0.51	0.56
D	6.00	6.10	6.20
D1	5.30REF		
E	6.50	6.60	6.70
E1	4.70	4.83	4.92
e	2.286BSC		
H	9.90	10.10	10.30
L	1.40	1.50	1.70
L1	2.90REF		
L2	0.51BSC		
L3	0.90	-	1.25
L4	0.60	0.80	1.00
L5	1.70	1.80	1.90
θ	0°	-	8°
$\theta 1$	5°	7°	9°
$\theta 2$	5°	7°	9°
K	0.40REF		

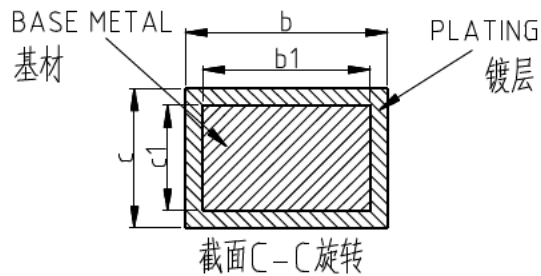
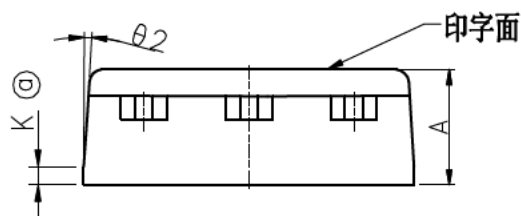
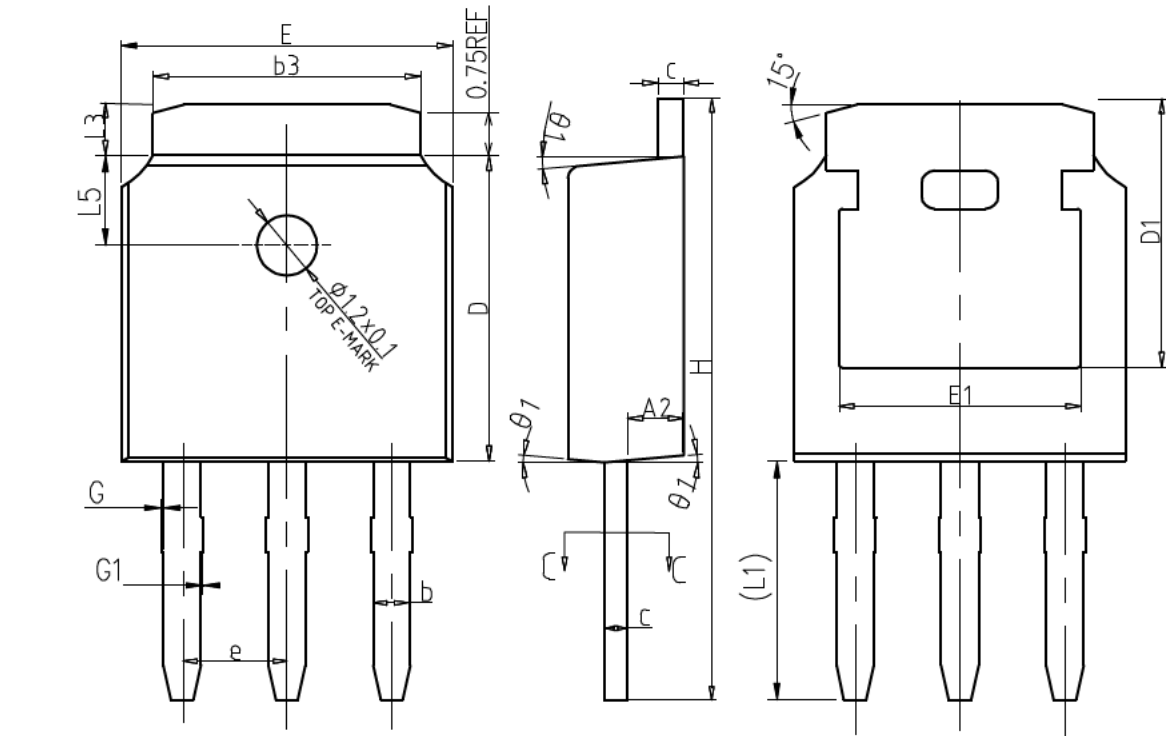
TO-251-3L(IPAK)



COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A2	0.97	1.07	1.17
b	0.72	0.78	0.85
b1	0.71	0.76	0.81
b3	5.23	5.33	5.46
c	0.47	0.53	0.58
c1	0.46	0.51	0.56
D	6.00	6.10	6.20
D1	5.30REF		
E	6.50	6.60	6.70
E1	4.70	4.83	4.92
e	2.286BSC		
H	16.10	16.40	16.60
L1	9.20	9.40	9.60
L3	0.90	1.02	1.25
L5	1.70	1.80	1.90
$\theta 1$	5°	7°	9°
$\theta 2$	5°	7°	9°
K	0.40REF		

TO-251-3L(SIPAK)



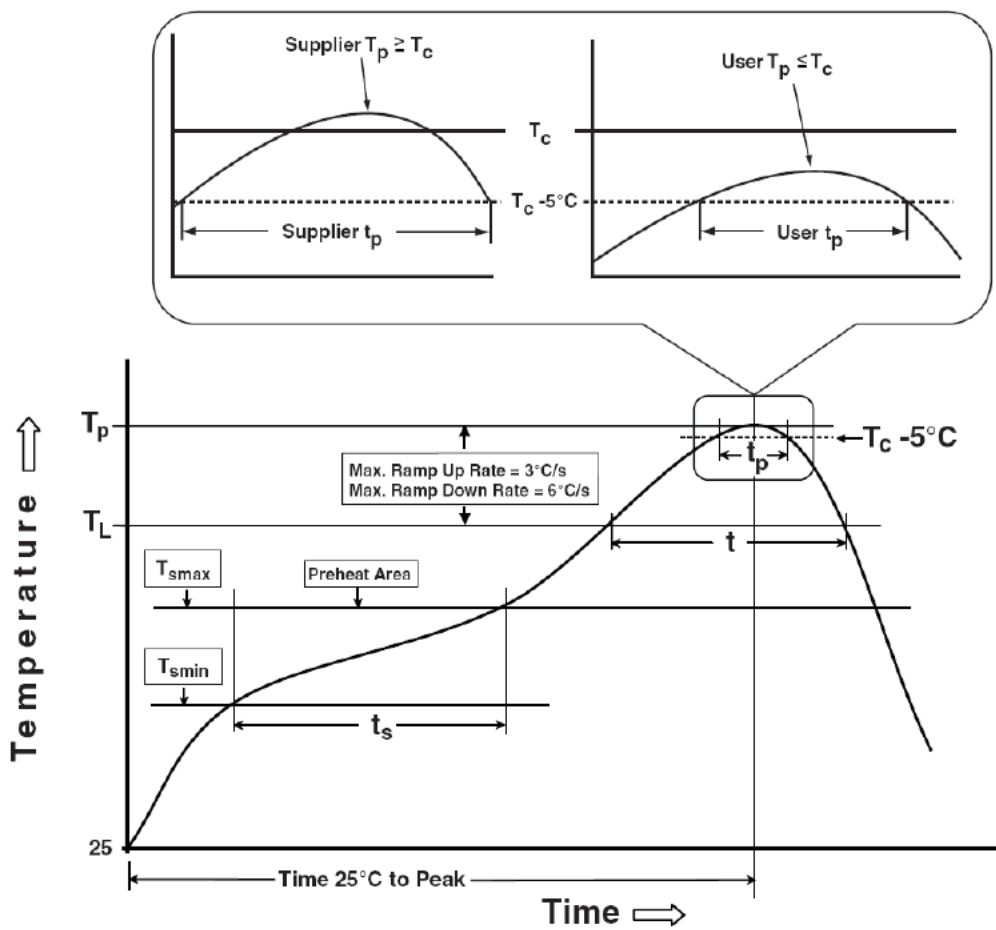
COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A2	0.97	1.07	1.17
b	0.72	0.78	0.85
b1	0.71	0.76	0.81
b3	5.23	5.33	5.46
c	0.47	0.53	0.58
c1	0.46	0.51	0.56
D	6.00	6.10	6.20
D1	5.30REF		
E	6.50	6.60	6.70
E1	4.70	4.83	4.92
e	2.286BSC		
H	10.00	11.20	11.40
L1	4.00	4.10	4.20
L3	0.90	1.02	1.25
L5	1.70	1.80	1.90
theta 1	5°	7°	9°
theta 2	5°	7°	9°
K	0.3	0.4	0.5

Devices Per Unit

Package Type	Unit	Quantity
TO-252-3L	Tube	72
TO-251-3L	Tube	72

Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HOLT	JESD-22, A108	1000 Hrs, Bias @ 125°C
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C